

022404

THE COMMISSIONER FOR PATENTS
P.O.Box 1450, Alexandria, VA 22313-1450

Docket No. TS02-115/320

U.S.P.T.O.

Transmitted herewith for filing is the Patent Application of:

Inventor: **YEU-DER CHIH**

For: **AN ARRAY STRUCTURE OF TWO-TRANSISTOR CELLS WITH MERGED FLOATING GATES FOR BYTE ERASE AND RE-WRITE IF DISTURBED ALGORITHM**

Enclosed are:

6 sheets of drawing(s) - formal.

An assignment of the invention to **Taiwan Semiconductor Manufacturing Co.**

An associate power of attorney Applicant claims small entity status

Request & Certification under 35 USC 122(b)(2)(b)(i)

The filing fee has been calculated as shown below:

	(Col. 1)	(Col. 2)	OTHER THAN A SMALL ENTITY	
FOR:	NO. FILED	NO. EXTRA	RATE	FEE
BASIC FEE				\$ 770.
TOTAL CLAIMS	41 -20=	21	x 18 =	\$ 378.
INDEP CLAIMS	8 -3=	5	x 86 =	\$ 430.
			SUB TOTAL	\$ 1,578.
			ASSIGNMENT	\$40.
			TOTAL	\$ 1,618.

Please charge my Deposit Account No. 19-0033 in the amount of \$ 1,618. A duplicate copy of this sheet is enclosed.

The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 19-0033. A duplicate copy of this sheet is enclosed.

Any additional filing fees required under 37 CFR §1.16.

Any patent application processing fees under 37 CFR §1.17.

Respectfully submitted,

 STEPHEN B. ACKERMAN, REG. NO. 37,761

EXPRESS MAIL CERTIFICATE

Express Mail No.EV385357788US

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail in an envelope addressed to Assistant Commissioner of Patents, Alexandria, VA, 22313-1450. Applicant and/or Attorney requests the date of deposit as the Filing Date.

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